

FIG. 1

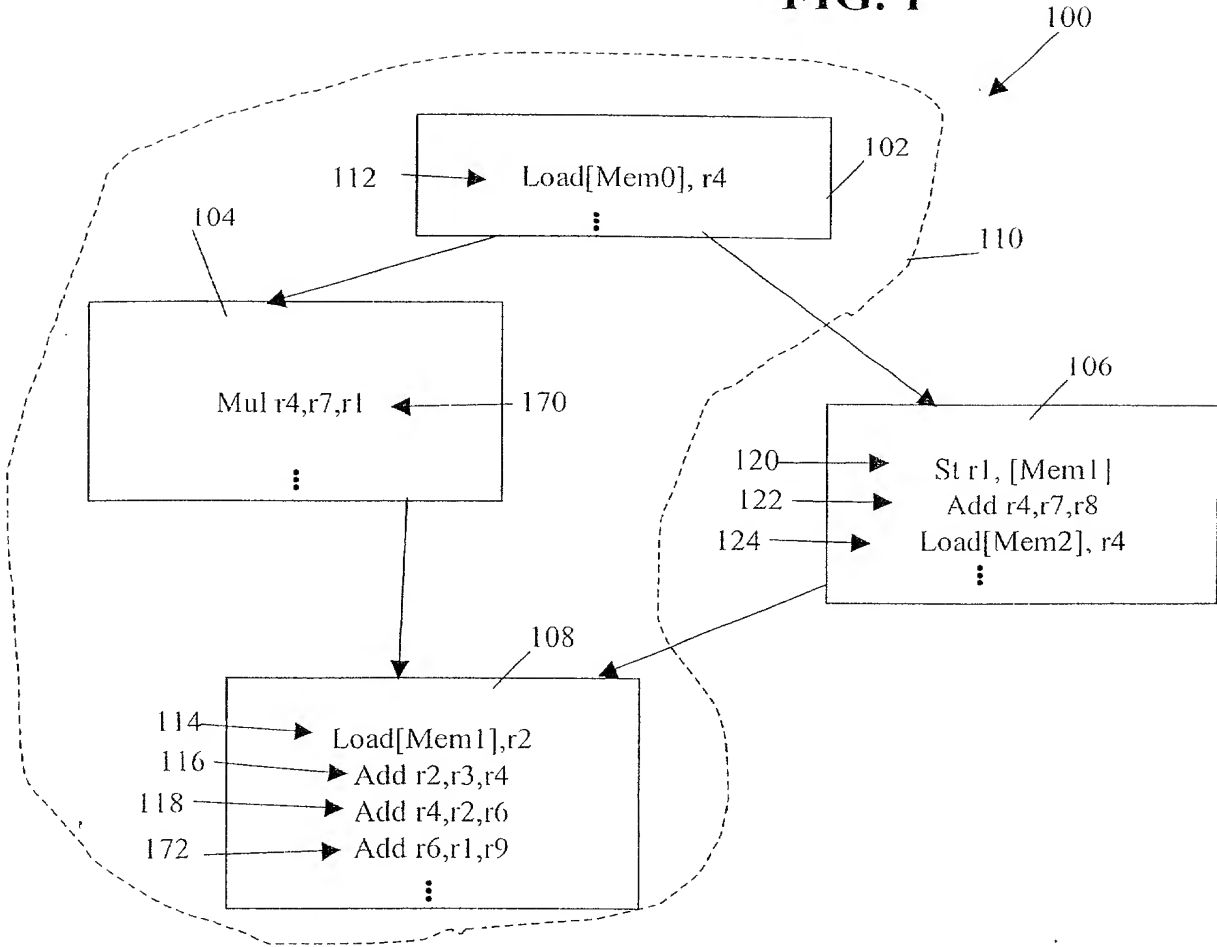
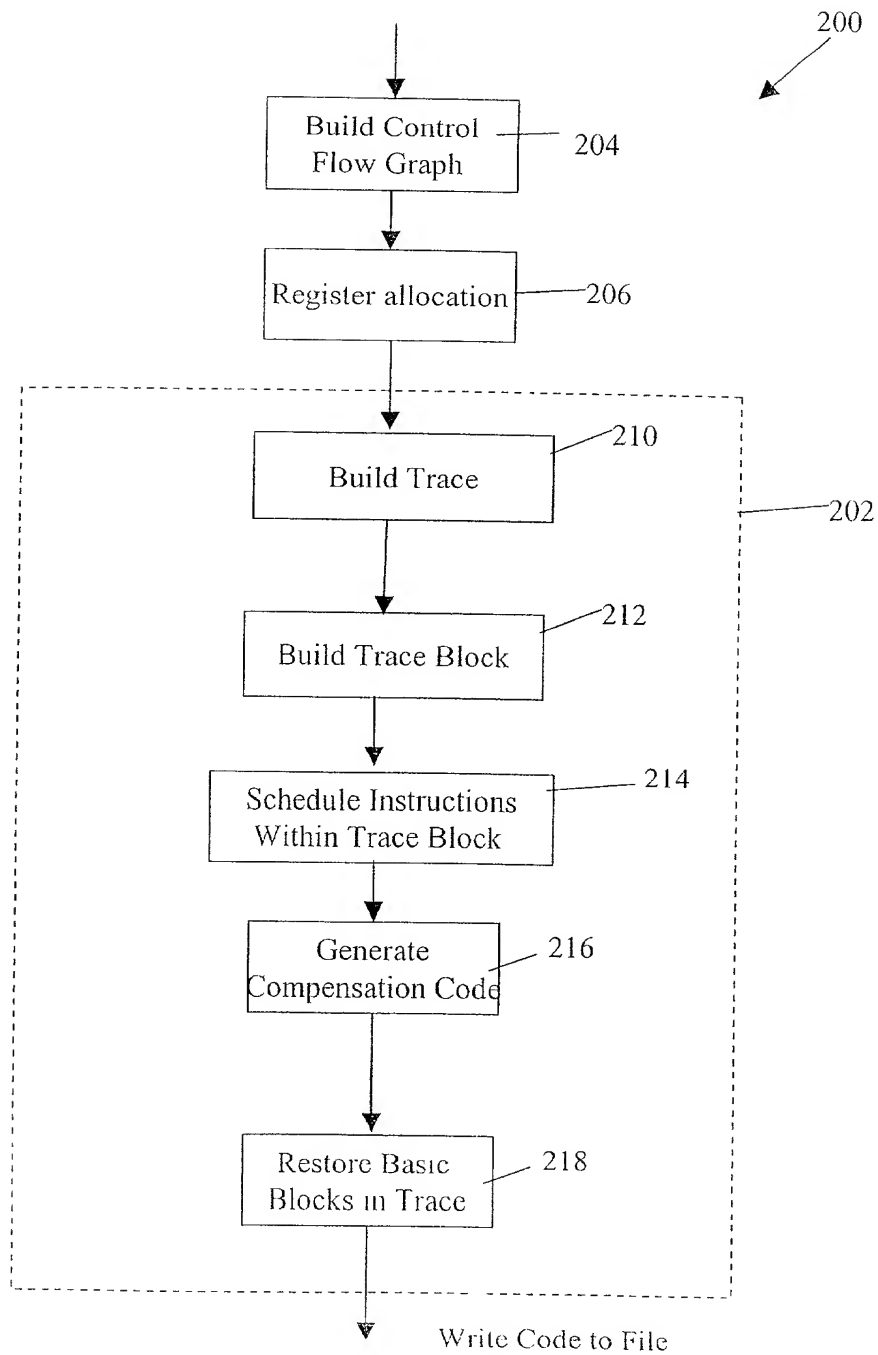


FIG. 2



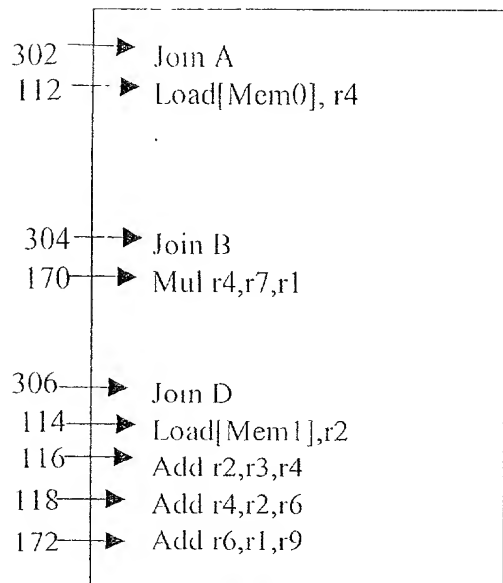


FIG. 3

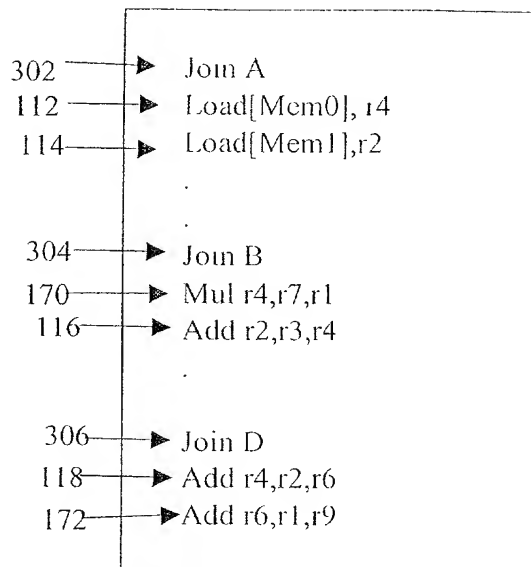


FIG. 6

Join B → Used {r1,r4,r7}

FIG. 5

FIG. 4A

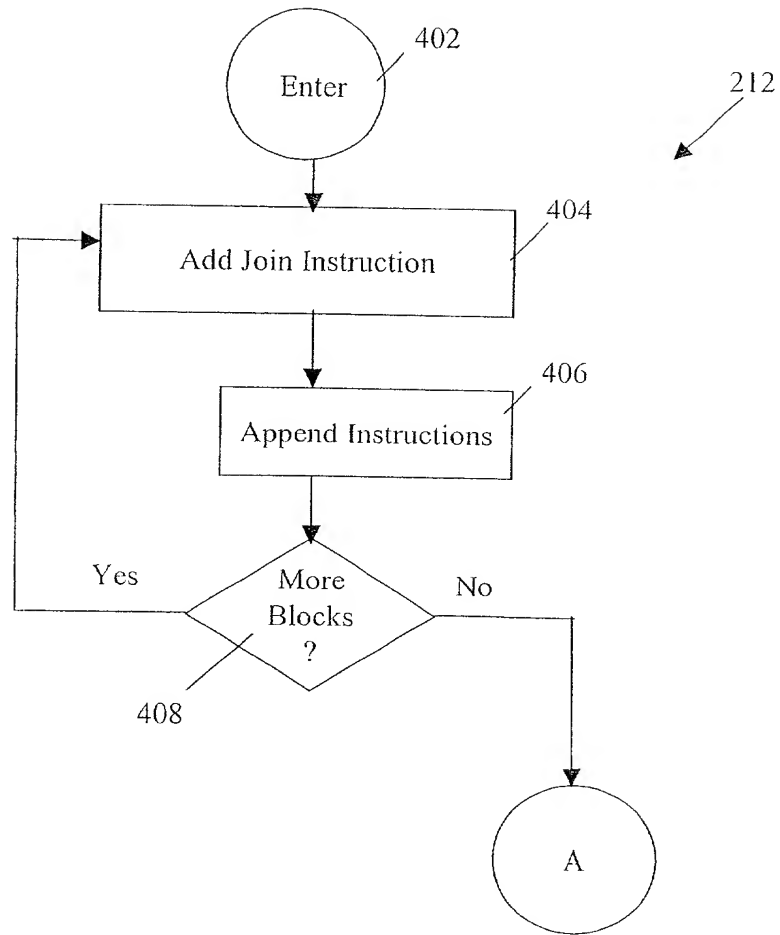
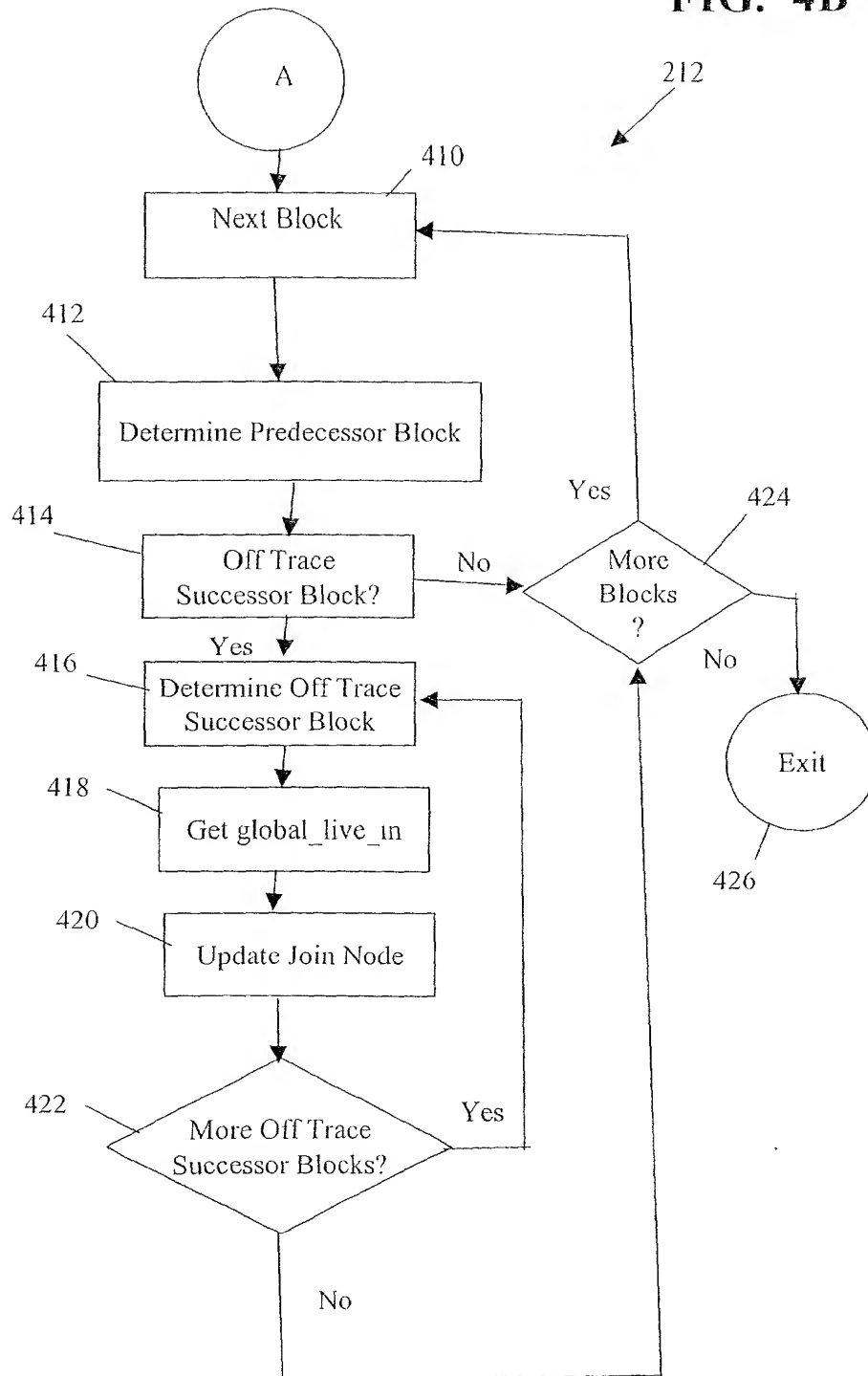
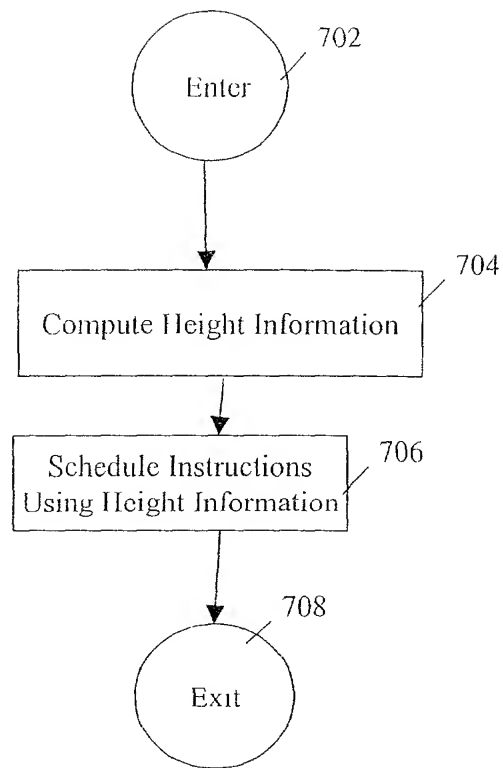


FIG. 4B



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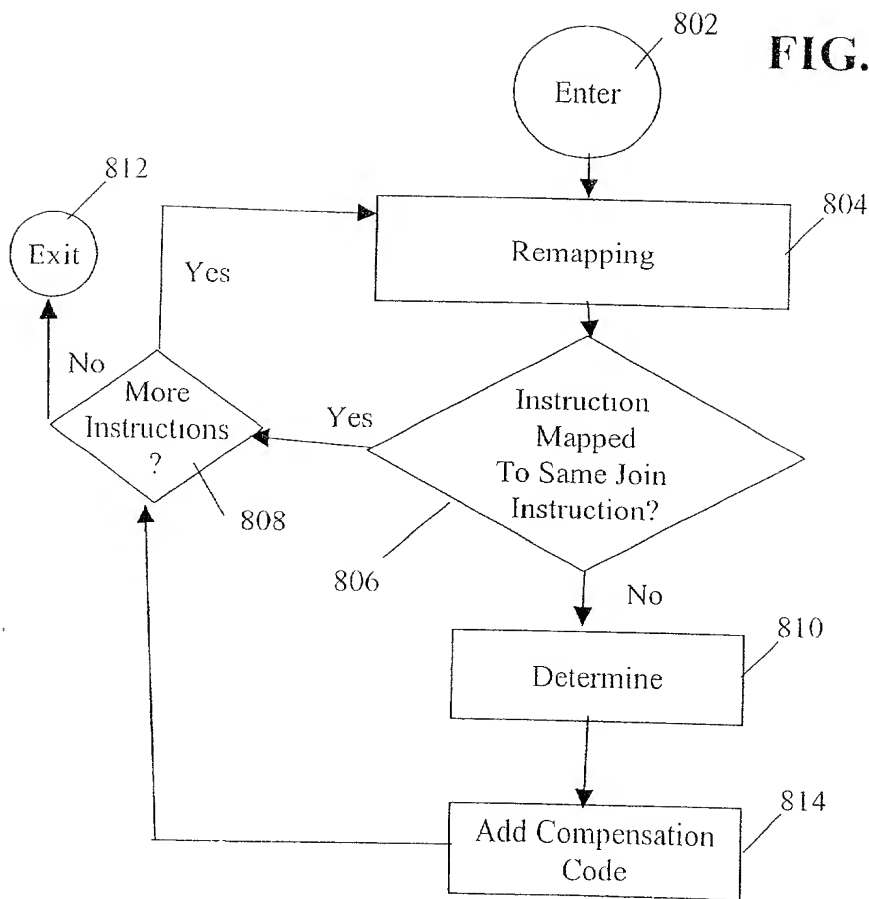


FIG. 8

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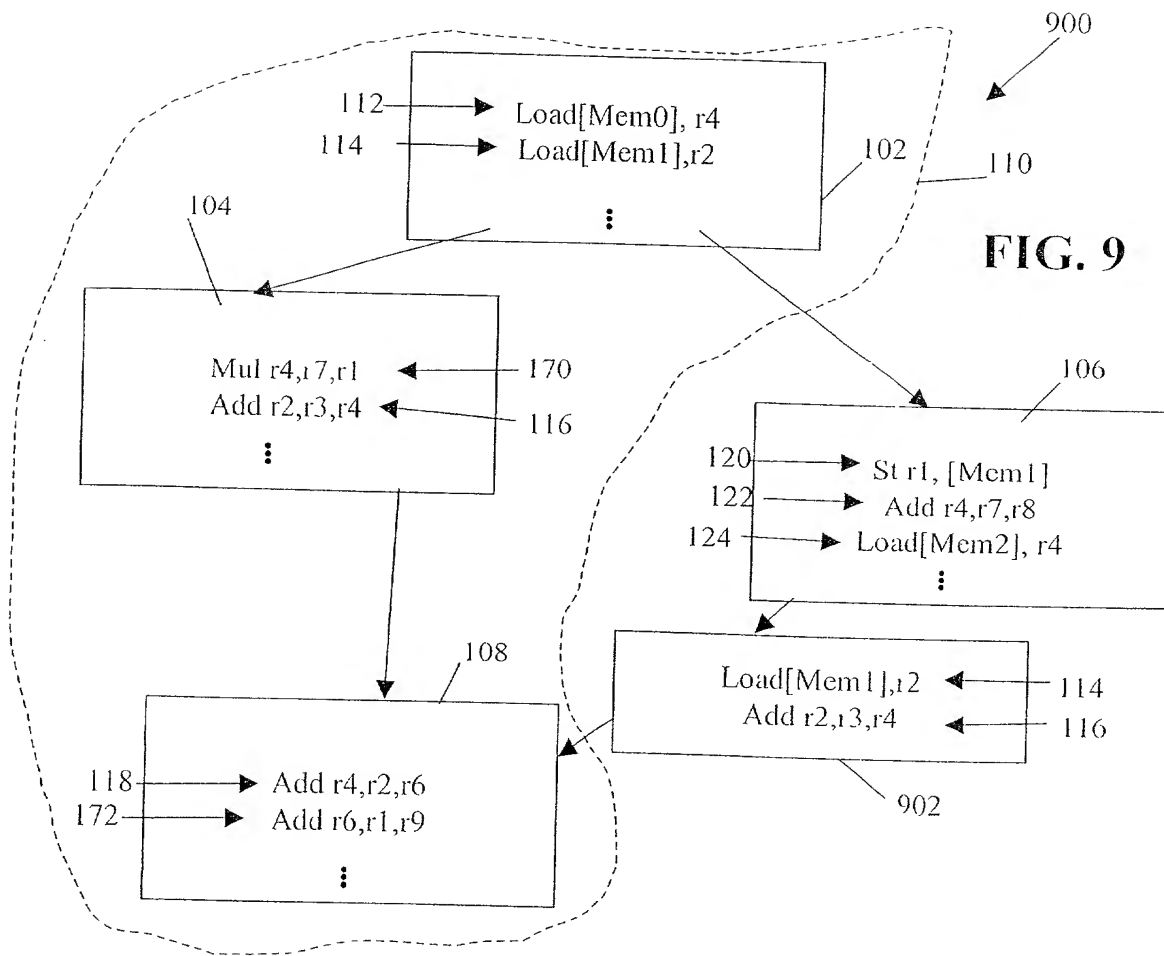


FIG. 9

FIG. 10

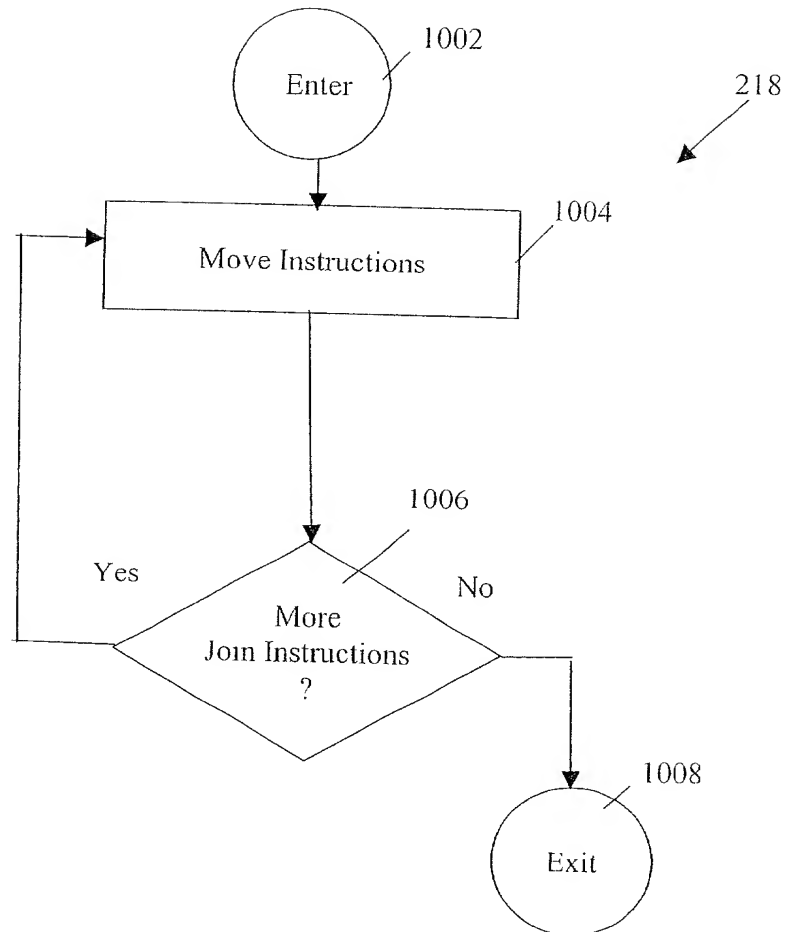


FIG. 11

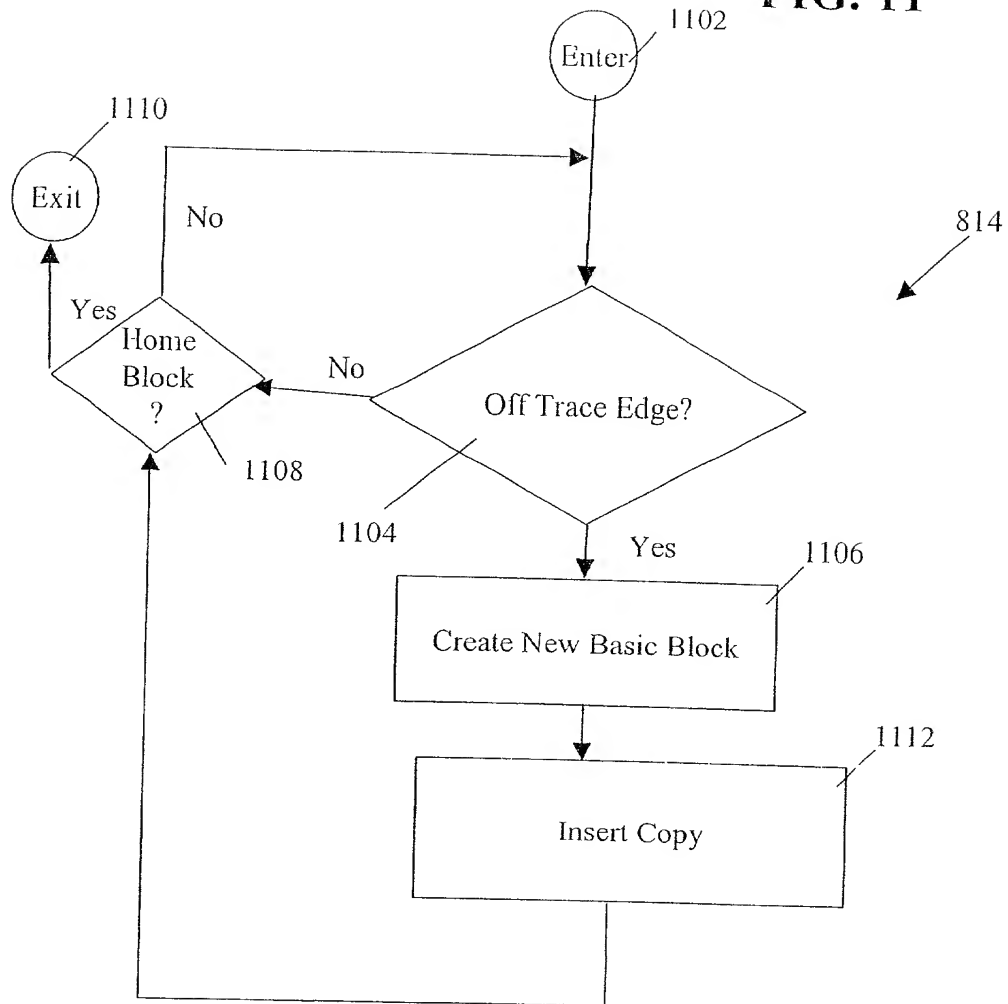


FIG. 12 is a block diagram of a computer system 1200. The system 1200 includes a processor 1202, a memory 1204, a storage device 1206, a ROM 1208, a bus 1210, a communication interface 1212, a display 1214, an input device 1216, and a cursor control device 1218. The processor 1202, memory 1204, storage device 1206, and ROM 1208 are connected to the bus 1210. The communication interface 1212 is also connected to the bus 1210. The display 1214, input device 1216, and cursor control device 1218 are connected to the system 1200. The system 1200 is connected to a network 1220, which includes a local network 1222 and the Internet 1224. A server 1230 and a host 1232 are also connected to the network 1220.

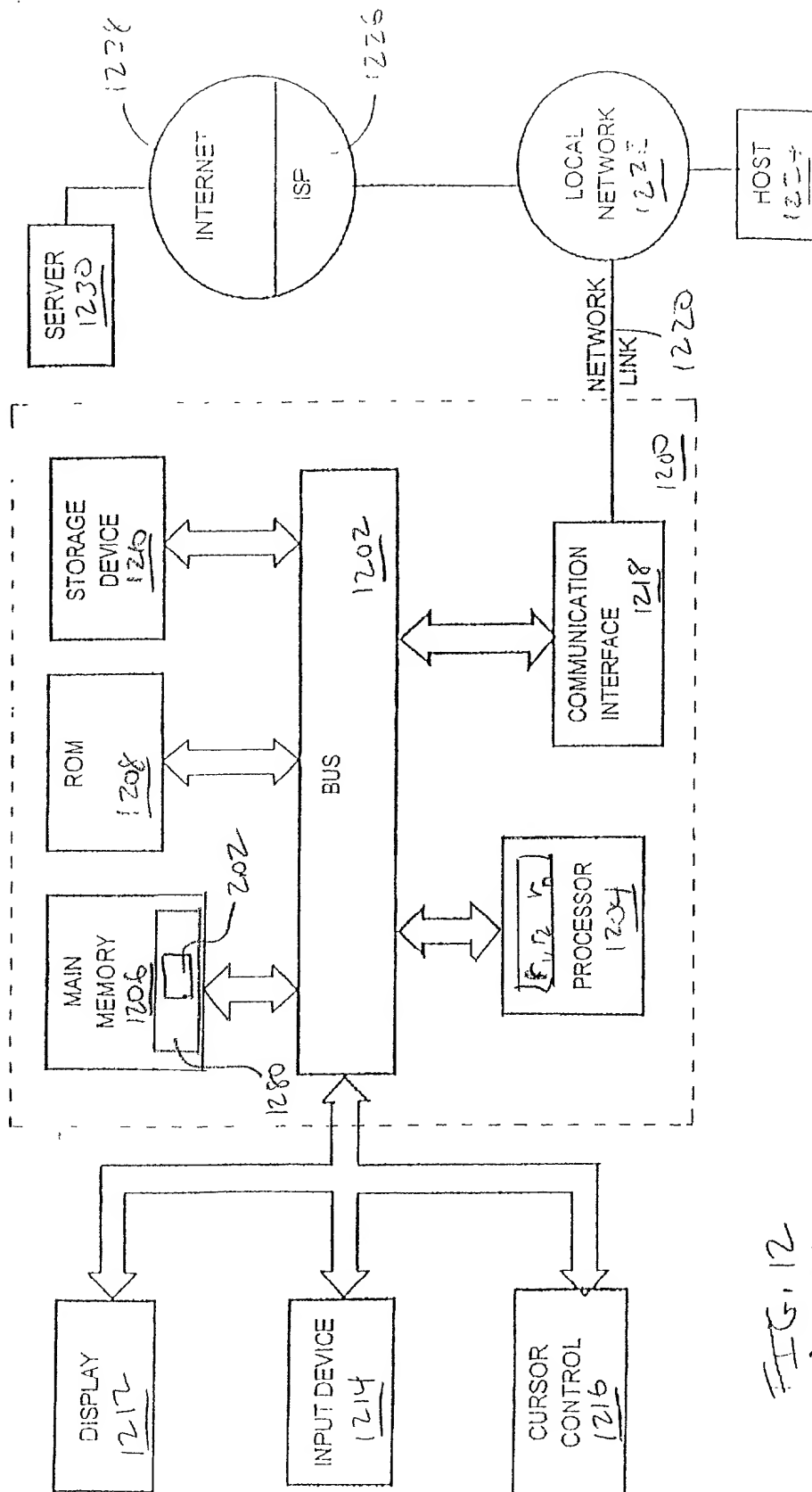


FIG. 12